THE UNIVERSITY OF BRITISH COLUMBIA
Department of Electrical Engineering

ELEC 466 : Digital Signal Processing

Winter Session 1995/96, Term 2

Instructor

Ed Casas. I am normally available only during lectures, tutorials and office hours. The best way to contact me at other times is by e-mail at `edc@ee.ubc.ca`.

Lectures

Tuesdays and Thursdays, 10:30 – 11:30 AM in MCLD 214 starting January 2 and ending April 4. No lectures/labs on February 20 – 22 due to mid-term break.

Office Hours

Tentatively scheduled for Tuesdays and Thursdays, 11:30 – 12:30 AM. in MCLD 155. May change depending on demand. See Web page for current times.

Tutorials

Wednesdays 4:30 – 6:30 pm in MCLD 214 starting on January 10. The tutorial format is still to be decided. It will probably involve software demonstrations and solutions to assignments.

Teaching Assistants

Mike Seymour, `seymour@ee.ubc.ca`.

Web Page

Students should check the course Web page `http://www.ee.ubc.ca/~elec466` regularly for announcements about the course. Copies of the lecture notes, assignments, etc will also be available in various formats. You can read these course pages using the text browser (lynx) which is available through Netinfo.

Prerequisites

Students should be familiar with analysis of linear time-invariant systems as covered in ELEC 359 including the properties of the Fourier Transform, convolution and sampling. Students should be thoroughly familiar with the use of complex numbers.

Text

Emmanuel C. Ifeachor and Barrie W. Jervis, *Digital Signal Processing : A Practical Approach*. Addison-Wesley, 1993.

Assignments

An assignment will be given out each week. Solutions will be made available for all questions but not all questions will be marked. Some assignments will require the use of the Matlab software available on the Department's Unix Workstations.

Evaluation

There will be one mid-term examination (1 hour) in mid- to late February (exact date TBD). The final mark will be calculated as follows:

final exam	60%
midterm exam	25%
assignments	15%

Outline

- discrete time system analysis (Chapters 1-4)
- FIR and IIR filter design (Chapters 5-7)
- applications (Chapters 8-12)