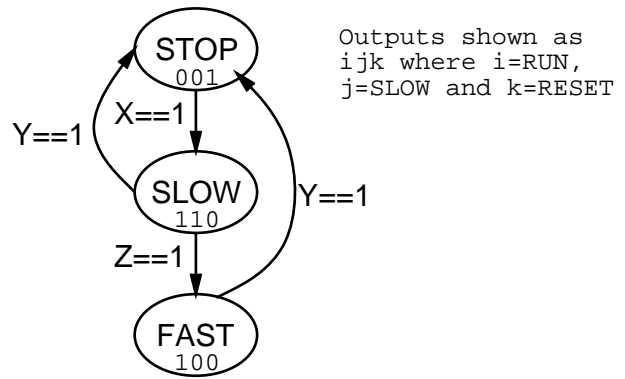


In this particular design the candy release signal will be high (equal to one) for one clock period (1 ms).

Question 2

1. The state transition diagram showing the possible states, the values of the outputs for each state and the transition conditions is shown below:



2. The outputs for each state are:

state	RUN	SLOW	RESET
STOP (00)	0	0	1
SLOW (11)	1	1	0
FAST (10)	1	0	0

where the values after each state name are the binary encodings of the state using two state variables, A and B .

3. A tabular description of the state transition diagram is as follows:

current state	input conditions			next state
	X	Y	Z	
STOP (00)	0	X	X	STOP (00)
STOP (00)	1	X	X	SLOW (11)
SLOW (11)	X	0	0	SLOW (11)
SLOW (11)	X	0	1	FAST (10)
SLOW (11)	X	1	X	STOP (00)
FAST (10)	X	1	X	STOP (00)

4. The sum-of-products boolean expressions for each output signal and for the signal giving the next state are:

$$RUN = A$$

$$SLOW = B$$

$$RESET = \overline{AB}$$

$$A' = \overline{AB}X + AB\overline{Y}$$

$$B' = \overline{AB}X + AB\overline{Y}Z$$

5. a schematic diagram for the controller is:

