number of bits in the DATA field,  $N_{DATA}$ ; and the number of pad bits,  $N_{PAD}$ , are computed from the length of the PSDU (LENGTH) as follows:

$$N_{SYM} = \text{Ceiling} \left( (16 + 8 \times \text{LENGTH} + 6) / N_{DBPS} \right)$$
 (18-11)

$$N_{DATA} = N_{SYM} \times N_{DBPS} \tag{18-12}$$

$$N_{PAD} = N_{DATA} - (16 + 8 \times LENGTH + 6)$$
 (18-13)

The function Ceiling (.) is a function that returns the smallest integer value greater than or equal to its argument value. The appended bits ("pad bits") are set to 0 and are subsequently scrambled with the rest of the bits in the DATA field.

An example of a DATA field that contains the SERVICE field, DATA, tail, and pad bits is given in L.1.5.1.

## 18.3.5.5 PLCP DATA scrambler and descrambler

The DATA field, composed of SERVICE, PSDU, tail, and pad parts, shall be scrambled with a length-127 frame-synchronous scrambler. The octets of the PSDU are placed in the transmit serial bit stream, bit 0 first and bit 7 last. The frame synchronous scrambler uses the generator polynomial S(x) as follows, and is illustrated in Figure 18-7:

$$S(x) = x^7 + x^4 + 1 ag{18-14}$$

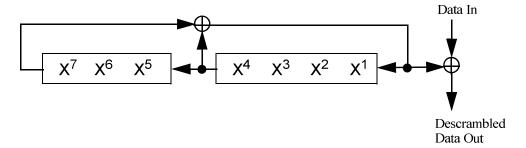


Figure 18-7—Data scrambler

An example of the scrambler output is illustrated in L.1.5.2.

## 18.3.5.6 Convolutional encoder

The DATA field, composed of SERVICE, PSDU, tail, and pad parts, shall be coded with a convolutional encoder of coding rate R = 1/2, 2/3, or 3/4, corresponding to the desired data rate. The convolutional encoder shall use the industry-standard generator polynomials,  $g_0 = 133_8$  and  $g_1 = 171_8$ , of rate R = 1/2, as shown in Figure 18-8. The bit denoted as "A" shall be output from the encoder before the bit denoted as "B." Higher rates are derived from it by employing "puncturing." Puncturing is a procedure for omitting some of the