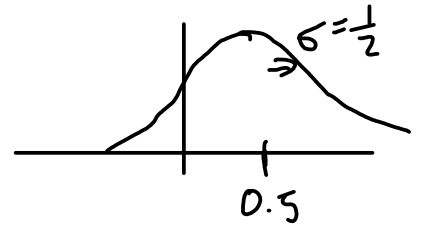
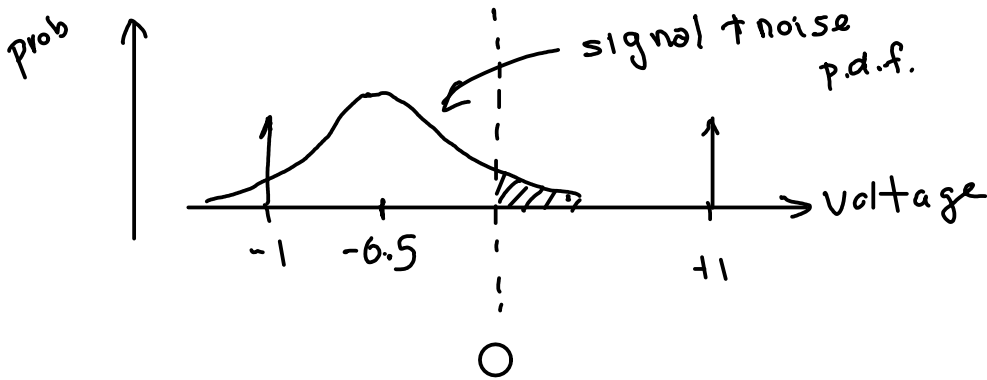


Baseband Transmitters and Receivers

Exercise 1: Gaussian noise with a mean of 0.5 V and a variance of $0.25 V^2$ is added to a bipolar signal with levels of ± 1 V. Assuming a decision threshold equally spaced between the two levels, what is the likelihood of error if +1 is transmitted? If -1 is transmitted? What is the average error rate if +1 is transmitted 25% of the time?

$$\mu = 0.5$$

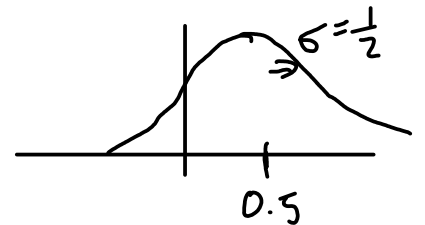
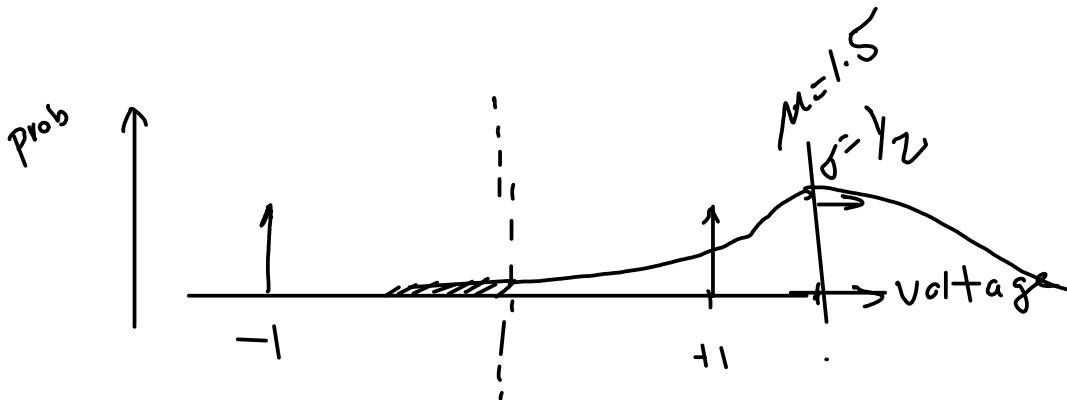
$$\sigma = \sqrt{\frac{1}{4}} = \frac{1}{2}$$



$$t = \frac{0 - (-0.5)}{\frac{1}{2}} = 1$$

$$P(\text{error} \mid -1 \text{ transmitted}) \approx 16\%$$

(1 - 84%)
from graph
in Lecture 3.



$$t = \frac{0 - 1.5}{\frac{1}{2}} = -3$$

$$P(\text{error} \mid +1 \text{ transmitted}) = 1 \times 10^{-3} \quad (0.1\%)$$

$$P(\text{error} | \dots) = 0.75 \times 0.16 + 0.25 \times 1 \times 10^{-3} =$$

$$0.00025$$

$$\approx 0.12 + 0.00025$$

$$= 0.12025$$

Exercise 2: What are the differential and common-mode voltages for this example?

$$1 \begin{cases} V_A = 5 \\ V_B = 0 \end{cases}$$

$$V_{\text{diff}} = 5 - 0 = +5$$

$$V_{\text{cm}} = \frac{5+0}{2} = 2.5$$

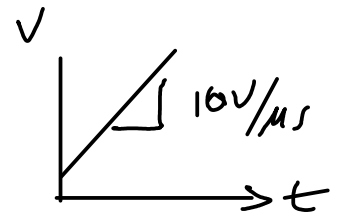
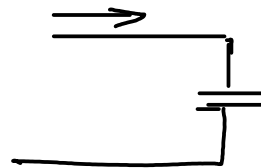
$$0 \begin{cases} V_A = 0 \\ V_B = 5 \end{cases}$$

$$V_{\text{diff}} = 0 - 5 = -5$$

$$V_{\text{cm}} = \frac{0+5}{2} = 2.5$$

Exercise 3: What is the current flowing into a 1nF capacitor if it is being charged at a rate of 10V/ μ s?

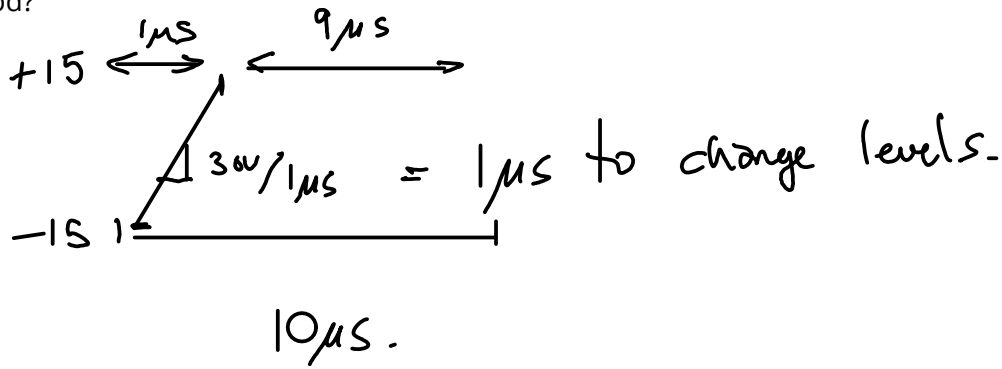
$$i = C \frac{dV}{dt}$$



$$\Delta Q = C \Delta V$$

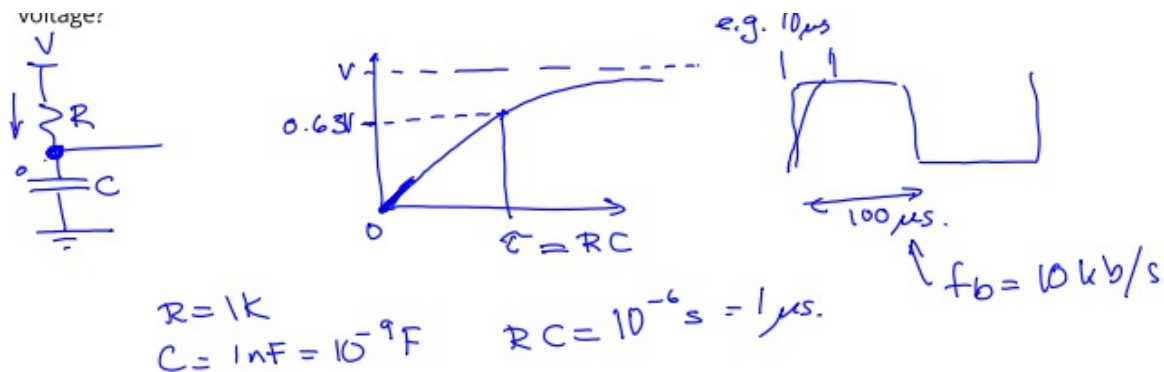
$$I = C \frac{\Delta V}{\Delta T} = 1 \times 10^{-9} \cdot 10 \times 10^6 = 10 \text{ mA}$$

Exercise 4: The RS-232 standard specifies a maximum slew rate of $30V/\mu s$. Assuming a voltage swing of 30 volts, what is the maximum data rate for which two signal level transitions occupy 10% of the bit period?



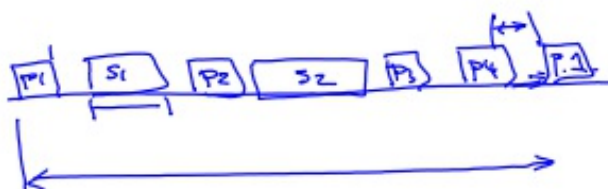
fastest rate $\approx \frac{1}{10\mu s} = 100kb/s$.

Exercise 5: If the capacitance of the transmission line joining several OC drivers is 1 nF and the pull-up resistor is 1 k Ω , how long will it take for the pull-up to pull the line from 0V to 63% of the logic high voltage?



Exercise 6: What are the consequences of increasing the delay between polls? What other factor might determine the maximum delay before slave gets access to the bus in a system using polling?

- delay between polls \rightarrow increases delay
- # of slaves
- amount of data
- data rate



Exercise 7: Consider a communication bus in a car that connects an airbag activation controller with a collision detector, a passenger-seat occupancy sensor and an airbag-disabling switch. Would it be more appropriate to use a polling- or contention-based bus arbitration protocol? Would it be appropriate for the arbitration protocol to allow different priorities for bus access? If so, what priorities might be assigned the different sensors?

use contention for lower delay
yes, priorities appropriate

collision sensor	↑ highest priority
occupancy sensor	↓ lowest priority
switch	

Exercise 8: If the common-mode circuit is used to carry 500mA, how much current flows through each half of the transformer secondary? What is the net effect on the flux in the transformer core?

$\approx 250 \text{ mA}$
no net effect

Exercise 9: When the input to the optocoupler is high, will the output be high or low? Assume a pull-up is connected to the output.

when input high, transistor is on
& output is pulled low.