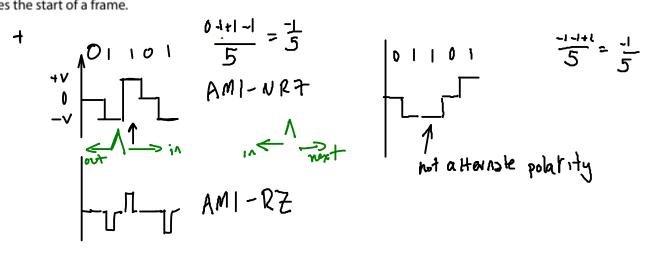
**Exercise 1:** Draw the waveform for an AMI-NRZ encoded sequence of bits '01101' assuming the previous mark was transmitted as a high. Draw the waveform assuming the second '1' indicates the start of a frame.



**Exercise 2**: What is the average DC value for the two cases in the previous exercise?

See above (no change, both 
$$-\frac{1}{5}V$$
)

Exercise 3: What might be some advantages of using a preamble? What might be some disadvantages?

adu.

- allows multiple protords to coexist on same medium

- allows backwords compatibility for new

signal & transy formats.

dis.

- overhead fixed at slowes + data rate

Exercise 4: By how much does the use of escape sequences slow down a link if random 8-bit characters are being transmitted? What is the overhead if a continuous stream of escape characters need to be sent over the link 2)

(2) ALL ESC total sut  $=\frac{2N}{N}=2$  Pater ate  $=\frac{1}{2}$ 

() PAV POM

P (any drow) = P JP = 1

method 1 assure N characters are sent 256 P= 1

 $P = \frac{1}{256}$ N are ESC 256

total =  $N + \frac{N}{256}$  (w/ ESC) = N (W/o Esc)

ratio of characters sent w/to sent w/o escapy 13  $\frac{N + \frac{N}{256}}{1}$ = 1+ 1/256 257 256 data rate = 256 257 original rate

prob. of having to send an extra character is 
$$\frac{1}{256}$$
.

# of transmitted on a vertex  $z = \frac{255}{256}$ .

 $\frac{1}{256} + \frac{1}{256} + \frac{1}{256} = \frac{1}{256}$ .

 $\frac{1}{256} + \frac{1}{256} + \frac{1}{256} = \frac{1}{256} = \frac{1}{256}$ .

 $\frac{1}{256} + \frac{1}{256} = \frac{1}{256} = \frac{1}{256} = \frac{1}{256}$ .

 $\frac{1}{256} + \frac{1}{256} = \frac{1}{256} = \frac{1}{256} = \frac{1}{256}$ .

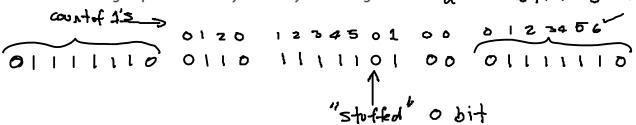
2 how

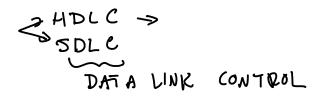
70%

10% 2 × 0.10 +1 x0.7 = 0.7 + 0.2 Exercise 5: Write out the complete sequence of 1's and 0's required to transmit the 12 bits 0110 1111 1100. Include the start and end flag sequences and any necessary bit stuffing.

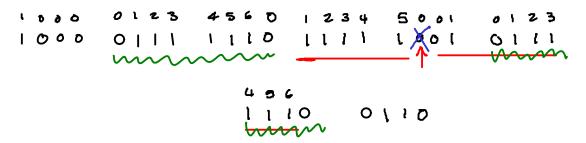
COUNTY 1'S

COUN





**Exercise 6**: An HDLC receiver sees the sequence 1000 0111 1110 1111 1001 0111 1110 0110. What data bits were contained within the frame?



**Exercise 7**: A physical layer transmits 3-bits per symbol. A frame of 128 bytes is being transmitted. How many padding bits will have to be added to the frame?

128 bytes 
$$\times$$
 8 bits/byte =  $2^{7} \cdot 2^{3} = 1024$  bits.

 $\frac{1024}{3 \text{ bits/symbol}} = 341.3$  symbols

round up to  $392$  symbols transmitted.

to tall bits 1x ansmitted (including padding) =  $342 \times 3 = 1026$ 

padding bits =  $1026 - 1024 = 2$ 

**Exercise 8**: How many states are required to implement a circuit that detects stuffed bits in an HDLC frame? How many bits of state are required?

10put=1

State number represents

consecutive 1's seen thus

Gar

FSM

count=5 and
input=?

## **Mid-Term Questions**

