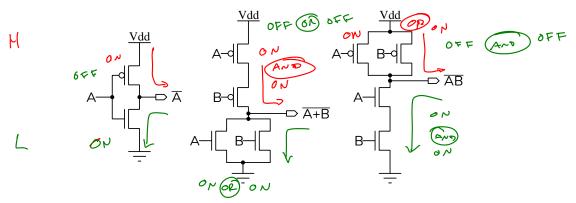
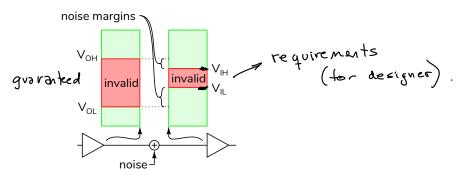
Implementation of Digital Logic Circuits

Exercise 1:



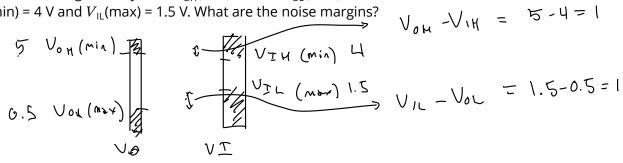
In which direction does the output current flow when the output is high? When it is low? Which transistors are on in each case?

Exercise 2:



Which of these specifications does the manufacturer guarantee? Which are requirements?

Exercise 3: A logic family has $V_{OH}(min) = 5 V$, $V_{OL}(max) = 0.5 V$, $V_{\rm IH}(\rm min)$ = 4 V and $V_{\rm IL}(\rm max)$ = 1.5 V. What are the noise margins?



Exercise 4: All else being equal, by how much would we expect to decrease power consumption when reducing logic levels from 5 V to 3.3 V? What would be the effect on power consumption in reducing the clock frequency from 50 MHz to 1 MHz?

$$V_{1} = 5 \qquad \frac{P_{2}}{P_{1}} = \left(\frac{3.3}{5}\right)^{2} \approx 0.444 \qquad P_{2} = 0.44P_{1}$$

$$V_{2} = 3.3 \qquad \frac{P_{1}}{P_{1}} = \left(\frac{3.3}{5}\right)^{2} \approx 0.444 \qquad P_{2} = 0.44P_{1}$$

$$F_{2} = 0.012 \qquad F_{1} = 0.56P_{1}$$

$$F_{1} = 50 \qquad F_{2} = 0.02$$

$$F_{2} = 0.62P_{1}$$

$$P_{2} = 0.62P_{1}$$

$$P_{2} = 0.62P_{1}$$

$$P_{1} = 0.98P_{1}$$

$$F_{1} = 0.98P_{1}$$

$$F_{2} = 0.62P_{1}$$

$$F_{1} = 0.98P_{1}$$

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Exercise 5: The energy stored in a battery (its "capacity") is measured in Watt-hours. If a circuit draws 100 mA for 100 µs per second and draws 100 mA the rest of the time, how long will a 1000 mAh battery last? Τ

$$\frac{100 \text{ m}}{100 \text{ m}} = \frac{0.1710^{-3}}{1} 100 + \frac{(1-0.1710^{-3})}{1} 100 \times 10^{-3}} \text{ mA}$$

$$= \frac{0.1710^{-3}}{1} 100 + \frac{(1-0.1710^{-3})}{1} 100 \times 10^{-3}} \text{ mA}$$

$$= 10 \times 10^{-3} + 100 \times 10^{-3} - 10 \times 10^{-6}$$

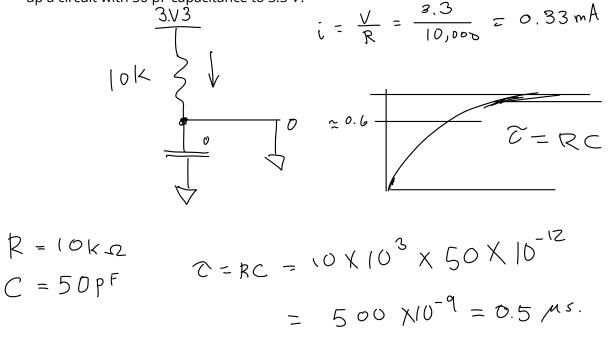
$$\approx 110 \times 10^{-3} \text{ mA} \approx 110 \text{ mA}$$

$$1000 \text{ mA} \cdot \text{ h} \cdot 3600 \text{ s/n} = 3.6 \times 10^{6} \text{ mA} \cdot \text{ s}.$$

$$+ 1000 \text{ mA} \cdot \text{ h} \cdot 3600 \text{ s/n} \approx 32.7 \times 10^{6} \text{ s} \approx 9000 \text{ hour}$$

$$(\approx 1900 \text{ hour})$$

Exercise 6: What are the active-state current and the RC time constant for a wired-or interrupt-request line using a $10k\Omega$ resistor pulling up a circuit with 50 pF capacitance to 3.3 V?



Exercise 7: How many square mm of PCB area does each package require? Which packages have their pins accessible when the package is placed on the PCB?

1

$$are_2 = \int (22)^2 = 484$$

 $((3.5)^2 = 12.25$ $Z \simeq 40 \times$