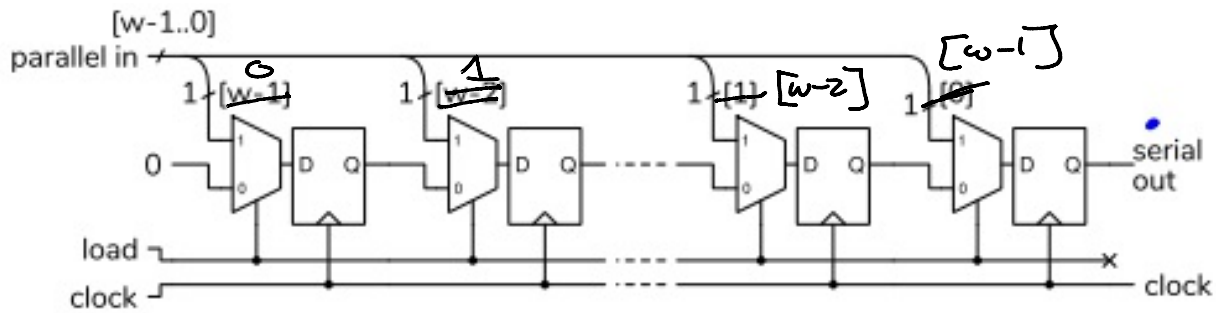
Interfaces

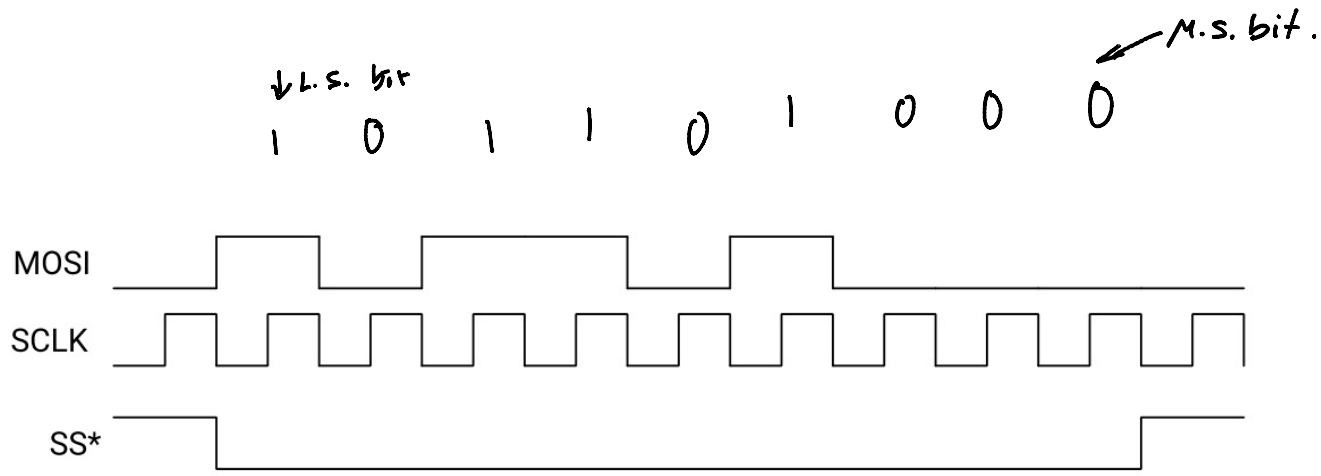
Exercise 1: How could you modify the circuit so that the bits are transmitted in order from the most-significant to the least-significant bit?

Connect rightmost S.R. bit to



asserted means True.

Exercise 2: The diagram below shows a transfer over an SPI bus. How many bits of data are transferred and what is the decimal value of this data?



0 0 0 1 0 1 1 0 1 ← m.s. bit first.

9 bits transferred (atypical)

in hex 02d = 45_{10}