Duling Lai

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Education

University of British Columbia

Jan 2017 - Present

- Master of Applied Science
- Research Area: Mobile Application Optimization

Simon Fraser University

- May 2011 Dec 2016 Bachelor of Applied Science - Electronic Engineering
- Business Administration Minor

Work Experience

ICT Product Engineer

May - Aug 2015

PMC-Sierra Inc. - Burnaby. BC

- Planned and performed characterization tests to assure products meet the specifications
- Automated tests using TCL scripts to collect statistical data for the product devices under various operating environments
- Analyzed statistical results and produced a quality report for the new DDR4 products
- Worked with cross-functional teams to effectively test new semiconductor products for production preparation

Hardware Coop

Jan - Aug 2014

Sierra Wireless Inc. - Richmond, BC

- Reviewed new PCB layout design to ensure the design was consistent with the requirement specified by hardware component provider
- Conducted quality assurance test on the prototype or newly manufactured wireless modules, which assured the quality to meet customer's expectation
- Provided recommendation report for new design, which helped the new design to achieve better heat transfer capacity

Projects

Location Privacy Analysis for Mobile Applications

Jun - Dec 2017

- Demonstrated that applications can steathily track the users' location with no permission granted
- Analyzed the top 1000 free Google Play applications to reveal that 4% of the applications are steathily collecting users' location data
- Presented qualitative insights to show that the current Android permission shceme cannot fully protect the users from privacy leaks and thus better scheme is desired

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Projects Continued

Analyzing Privacy Leaks in Android Wear

May - Dec 2017

- Presented the first research in research community on analyzing Android Wear privacy leaks
- Revealed leakage of hardware identifiers in popular Android Wear applications
- Provided strategies that could be used to mitigate this privacy issue

UART Performance Monitor

ENSC 351 Project

Sep - Nov 2014

- Developed real-time programming skills through writing VGA and UART drivers that run on an embedded Linux system (PetaLinux)
- Created a software rendering framework in the ARGB color space, which could display images and text to the screen of the FPGA board
- Constructed a VGA serial console that displayed runtime performance characteristics and metrics of the UART interface on the screen

Skills

Hardware

- ☑ Patheon PCB design
- Hyperterminal
- ✓ VHDL and Haves Cmd Set
- ✓ Cadence Encounter
- ☑ LT Spice Circuitry Design
- Hardware Test and Debug

Programming

- ✓ Java
- Soot/ASM
- Android Development
- ✓ MATLAB
- ▼ TensorFlow

Basic Skills

- Soldering Technique
- ☑ Database Management
- Project Management
- ☑ Bilingual English & Mandarin☑ Proficient Linux User
- ☑ Photoshop & MS Office

Activities

- Teaching Assistant
- SPLASH 2017 Student Volunteer
- Illuminate Vancouver Guest Speaker

Sep - Dec 2017

Oct 2017

May 2017

Awards

- UBC International Tuition Award
- SFU Open-Scholarship For Academic Performance
- SFU Entrance Scholarship
- Dean's Honour Roll

Jan 2017 - Present

Sep - Dec 2012

May 2011 - Apr 2012