74LS181

74LS181 - 4-Bit ALU

ls181.vxi
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Module 0

Pod 0: N/C N/C N/C
Timing Pod 0/01 [Timing]

Port 0

7:	N/C	Unused Control 7
6:	N/C	Unused Control 6
5:	N/C	Unused Control 5
4:	N/C	Unused Control 4
3:	N/C	Unused Control 3
2:	N/C	Unused Control 2
1:	N/C	Unused Control 1
0:	N/C	Unused Control 0
RDY:	N/C	External Signal!
TRG:	N/C	External Signal!

Port 1

9:	N/C	Unused Q 9
8:	N/C	Unused Q 8
7:	N/C	Unused Q 7
6:	N/C	Unused Q 6
5:	N/C	Unused Q 5
4:	N/C	Unused Q 4
3:	N/C	Unused Q 3
2:	N/C	Unused Q 2
1:	N/C	External Signal!
0:	N/C	External Signal!

Module 1

Pod 2: D Outside Left
Data Pod 2/23 [Pattern I/O]

Port 2

0:	Output	A0
1:	Output	A1
2:	Output	A2
3:	Output	A3
4:	Output	B0
5:	Output	B1
6:	Output	B2
7:	Output	B3
~OE:	N/C	External Signal!
CLK:	N/C	External Signal!

Port 3

0:	Output	M
1:	Output	Cn
2:	Output	S0
3:	Output	S1
4:	Output	S2
5:	Output	S3
6:	N/C	Unused Pin 31
7:	N/C	Unused Pin 32
~OE:	N/C	External Signal!
CLK:	N/C	External Signal!

Pod 1: D Outside Right
Data Pod 1/01 [Pattern I/O]

Port 0

0:	Input	F0
1:	Input	F1
2:	Input	F2
3:	Input	F3
4:	N/C	Unused Pin 5
5:	N/C	Unused Pin 6
6:	N/C	Unused Pin 7
7:	N/C	Unused Pin 8
~OE:	N/C	External Signal!
CLK:	N/C	External Signal!

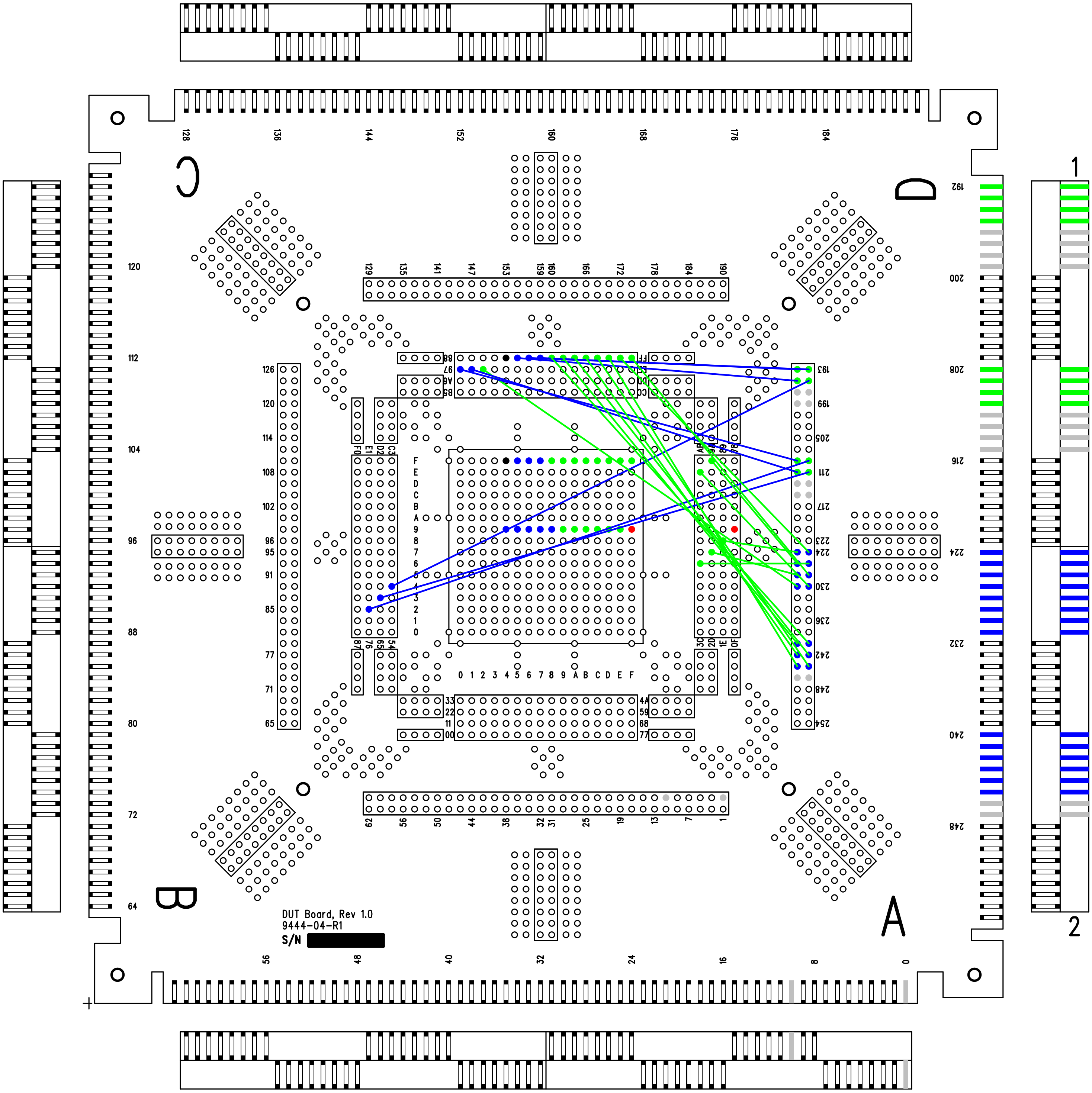
Port 1

0:	Input	Cn+4
1:	Input	P
2:	Input	G
3:	Input	A=B
4:	N/C	Unused Pin 13
5:	N/C	Unused Pin 14
6:	N/C	Unused Pin 15
7:	N/C	Unused Pin 16
~OE:	N/C	External Signal!
CLK:	N/C	External Signal!

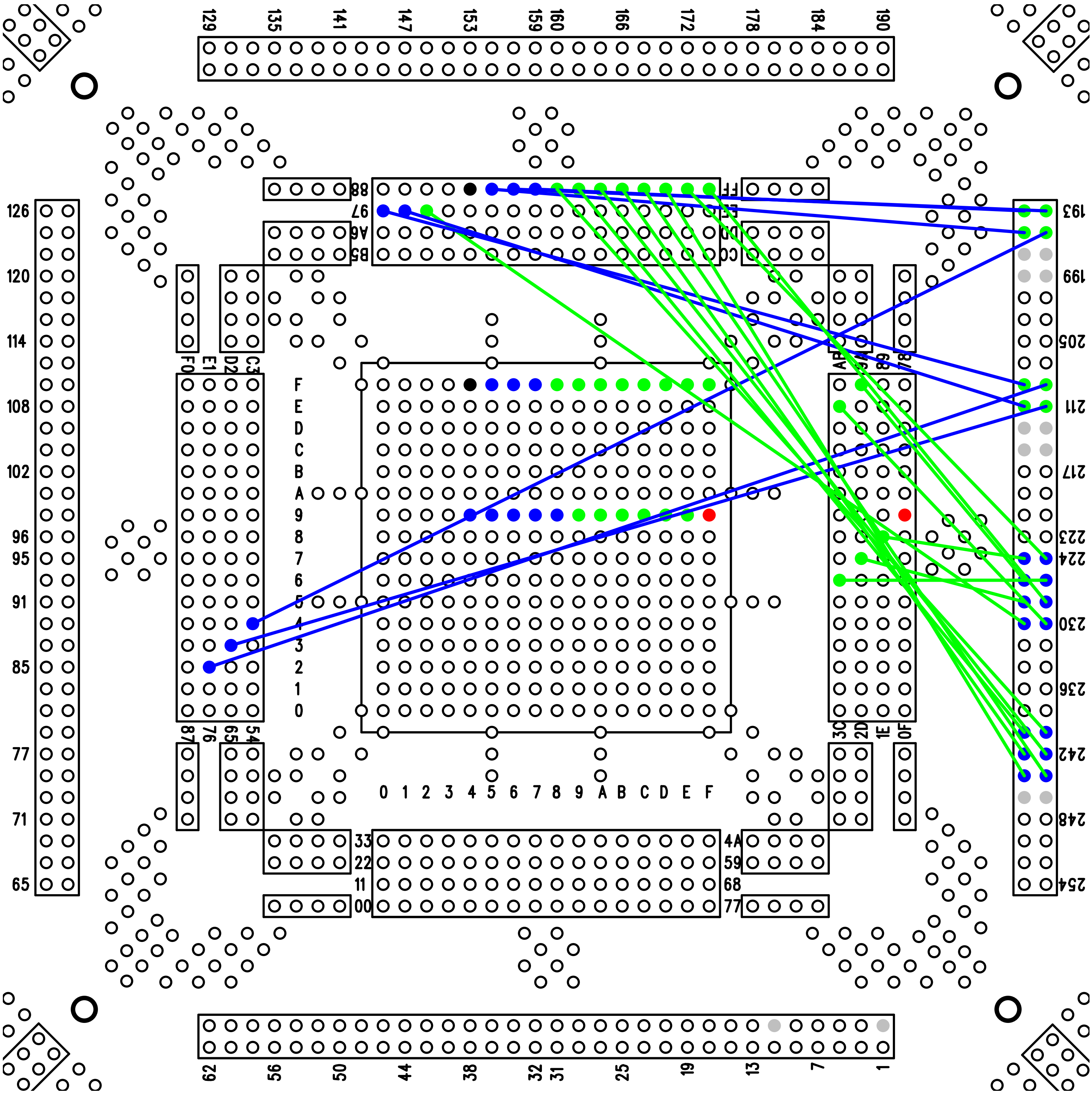
IC	Signal Name	IWA	OWA	POD_##
1	B0	FF	228	2:4 2
2	A0	FE	224	2:0 2
3	S3	FD	245	3:5 2
4	S2	FC	244	3:4 2
5	S1	FB	243	3:3 2
6	S0	FA	242	3:2 2
7	Cn	F9	241	3:1 2
8	M	F8	240	3:0 2
9	F0	F7	192	0:0 1
10	F1	F6	193	0:1 1
11	F2	F5	194	0:2 1
12	GND	F4		
13	F3	94	195	0:3 1
14	A=B	95	211	1:3 1
15	P	96	209	1:1 1
16	Cn+4	97	208	1:0 1
17	G	98	210	1:2 1
18	B3	99	231	2:7 2
19	A3	9A	227	2:3 2
20	B2	9B	230	2:6 2
21	A2	9C	226	2:2 2
22	B1	9D	229	2:5 2
23	A1	9E	225	2:1 2
24	VCC	9F		

IC Signal Name IWA OWA POD_##

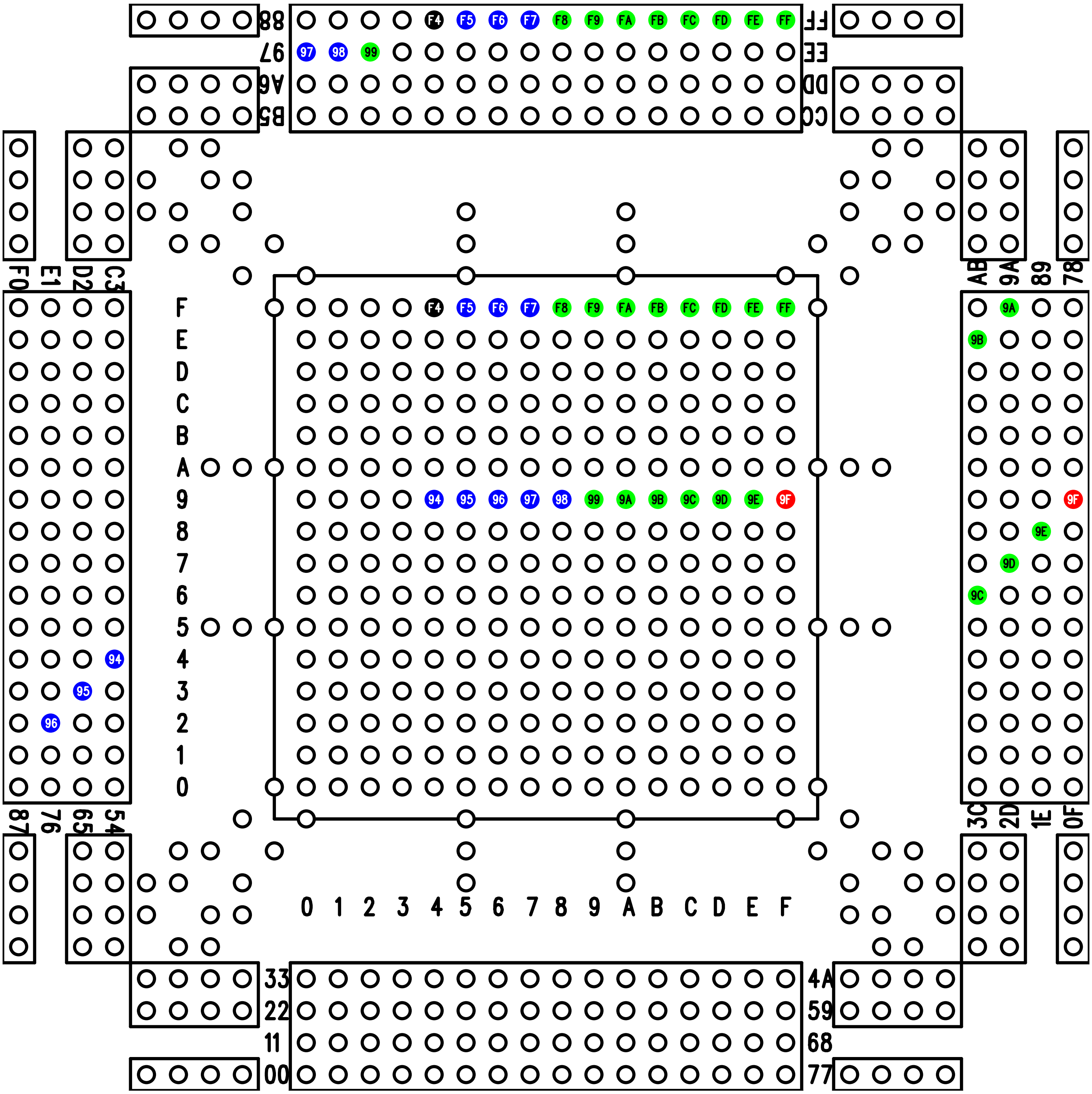
Connector - Pod View



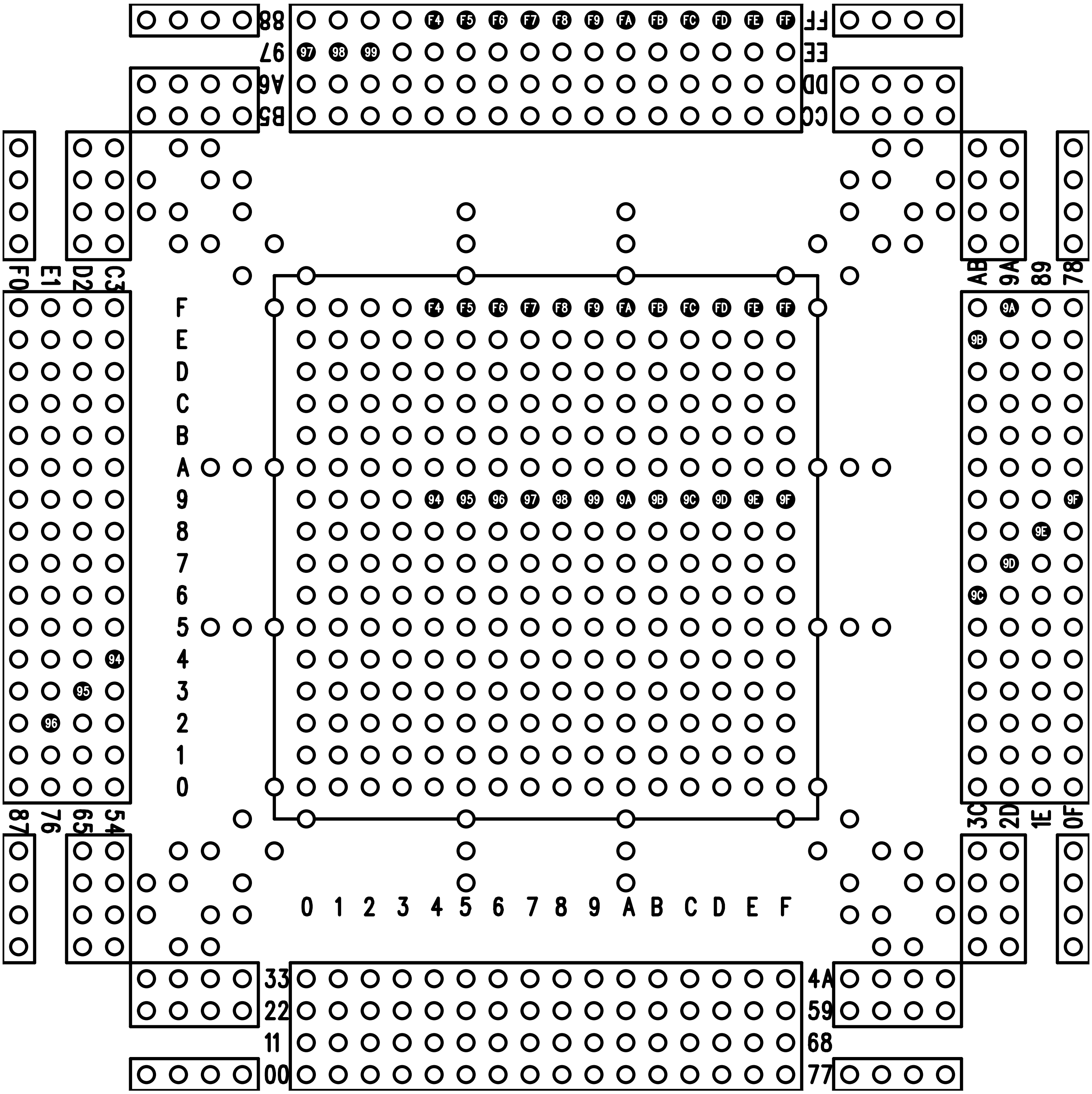
Inner Wiring Array - Outer Wiring Array View



Inner Wiring Array Reference View (Colour)



Inner Wiring Array Reference View (B&W)



IC - PGA Socket View