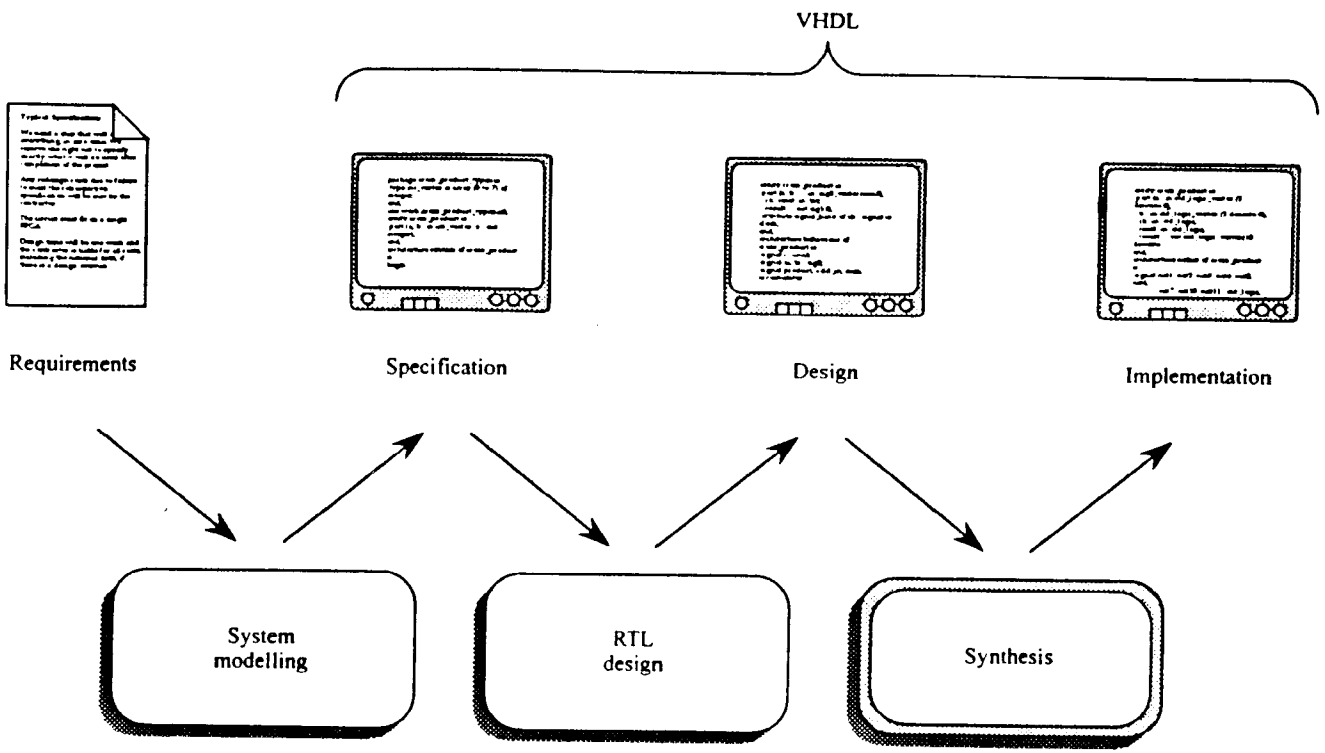

Figure 1.1 The VHDL-based hardware design cycle.