SHAURYA PATEL

6335 Thunderbird Crescent & Vancouver, BC V6T2G9

EDUCATION

University of British Columbia, Vancouver	2021 - Present
Ph.D. in Electrical and Computer Science & Engineering	
Advised by Dr. Alexandra Fedorova & Dr. Margo Seltzer	
University of Massachusetts, Amherst	2019 - 2021
M.S. in Computer Science	
GPA - 3.71	
Nirma University, Ahmedabad	2013 - 2017
B.Tech in Computer Engineering	
GPA - 8.46	

PUBLICATIONS

CHERI-picking: Leveraging capability hardware for prefetching	2023
Shaurya Patel , Sidharth Agrawal, Alexandra (Sasha) Fedorova, Margo Seltzer Programming Languages and Operating Systems workshop (PLOS) @ SOSP'23	
FreeLunch: Compression-based GPU Memory Management for Convolutional Neural Networks	2021

PROFESSIONAL EXPERIENCE

•	Google - Ghost scheduler team	Research Intern
	Sep 2022 - Dec 2022	Vancouver, BC (remote)
	· Identified required components of the Linux completely fair scheduler to implement a minimum CFS policy.	

- Implemented a working CFS policy based on the components identified in the Ghost framework in eBPF.
- · Identified key future components to implement like Load balancing.

Google

SDE Intern

May 2020 - Aug 2020 Amherst, MA (remote)

Aug 2017 - Jul 2019

Mumbai, India

- Updated a cache library for indexing location data being used by multiple teams to reduce latency by upto 6% across multiple projects.
- Added a feature to low-latency serving infrastructure at google to enable faster auctions of ads to be served by early rejection of 5% of total ads.

Morgan Stanley

Senior Associate

- Optimized the scalability distributed trading platform. Used containerization, machine learning and improved load balancing. Successfully increased throughput by 300%.
- Developed a distributed platform to calculate the fees for a given trade being processed using Gemfire. This increased effciency of business processes by 200%.

RESEARCH EXPERIENCE

Memory management and prefetching

- · I'm interested in memory management in general and solving the problem of performance degrdation because of using far memory in datacenters.
- I'm looking at how to design and create a generalized pointer prefetcher in an operating system kernel using capability hardware.
- · I'm also exploring how memory page prefetching can be modeled as a Machine Learning problem.

FreeLunch

- $\cdot\,$ Developed a novel memory management technique for ML training that uses compression.
- FreeLunch has up to 35% less memory consumption and up to 70% better throughput than swapping and recomputation.

Automatic Thread Scheduler

- $\cdot \,$ Proved with benchmarks the effect of scheduling on thread co-degradations.
- \cdot Created a scheduler that automatically learns the groups of threads to schedule based on assigned rewards.
- $\cdot~$ The scheduler has 10% higher throughput as compared to the CFS by setting affinity of threads.

SERVICE

- Submission Chair, Eurosys 2023.
- VP Social External 2022 2023, ECEGSA.
- Volunteer, Splash 2020.

AWARDS AND GRANTS

- SOSP student scholarship, 2023
- OSDI student scholarship, 2021
- UBC President's Academic Excellence Initiative Award, 2021-2023
- Morgan Stanley Global Tech Excellence Award, 2019

TECHNICAL STRENGTHS

Computer LanguagesC++, C, JavaOperating systemsLinux, FreeBSD, CheriBSD.

Aug 2020 - May 2021

October 2021

October 2021 - Current